Docket No.: 174/173 CON 3

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James Schleicher et al. Docket No.: 174/173 CC Concurrently Herewith INTERCONNECTION RESOURCES FOR PROGRAMMABLE LOGIC INTEGRATED CIRCUIT DEVICES

Andrew Van Court, Reg. No. 48,506

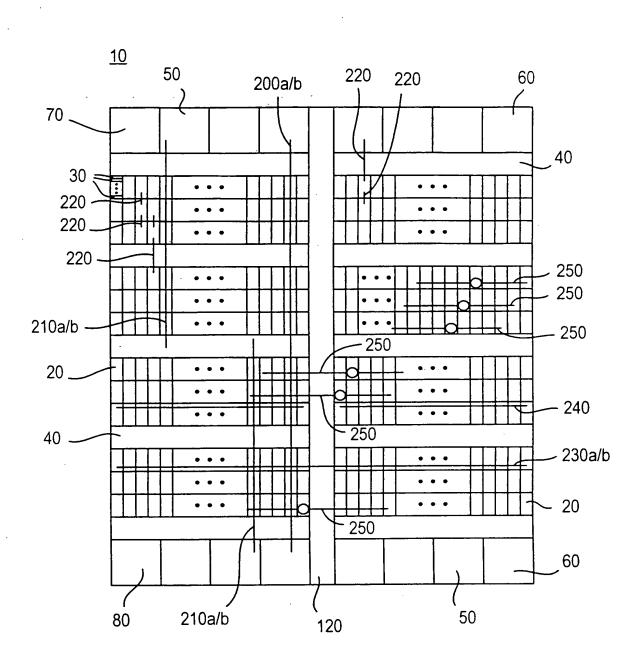


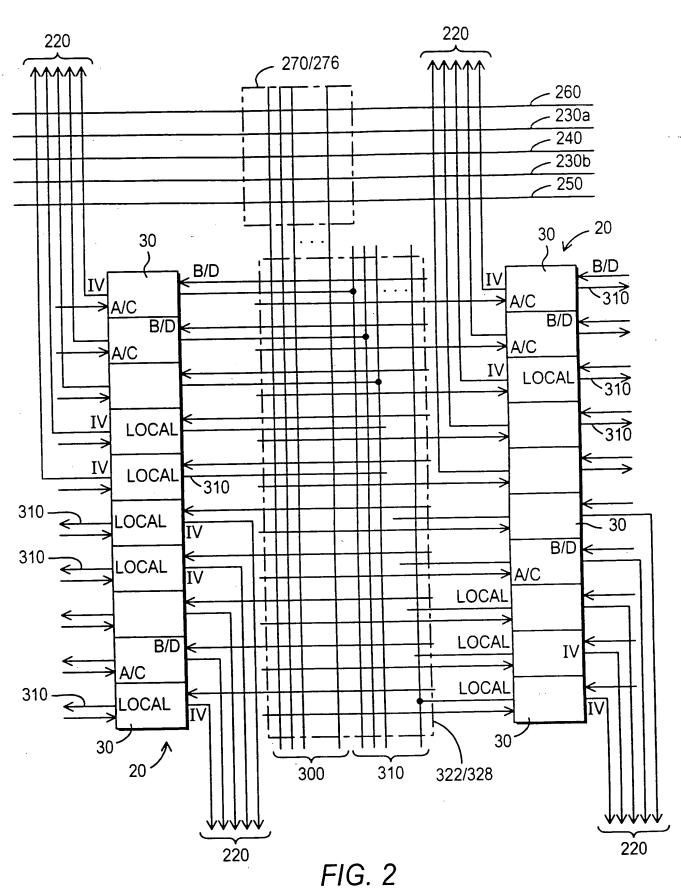
FIG. 1

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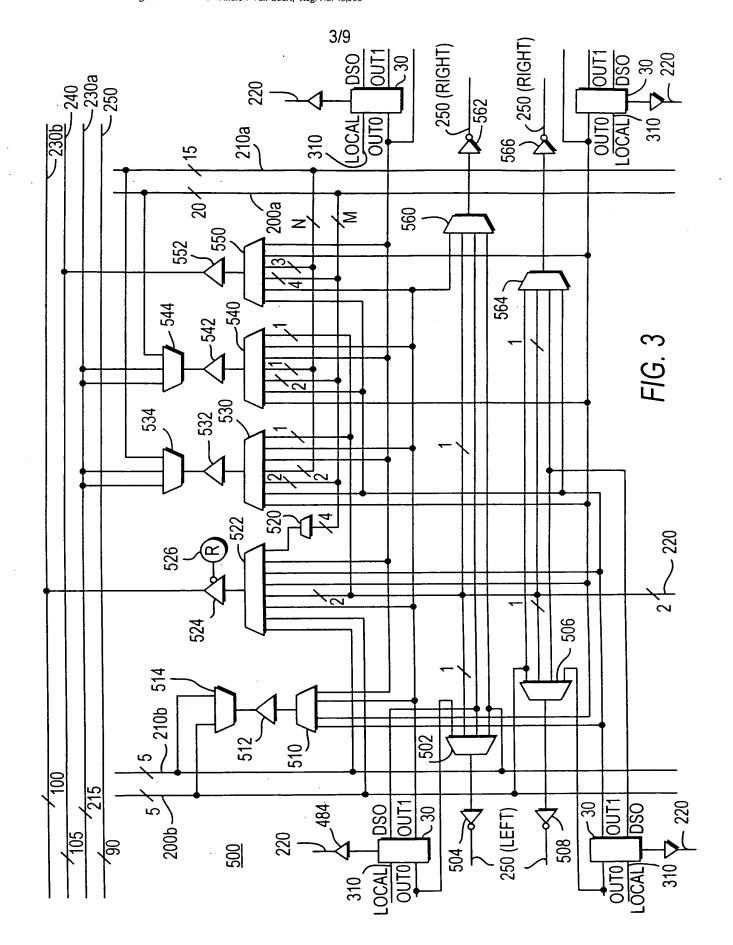


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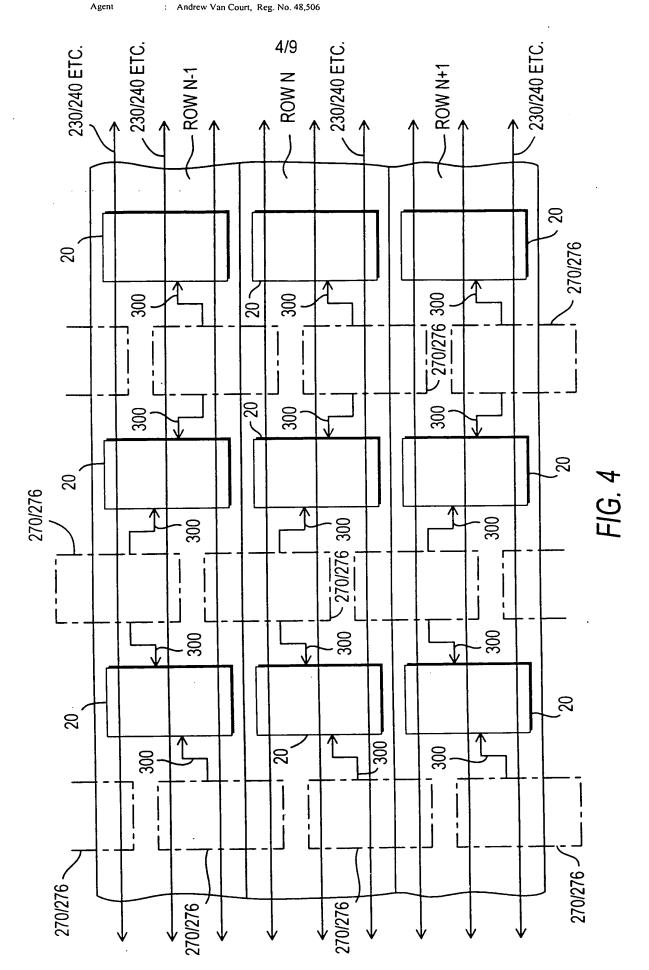


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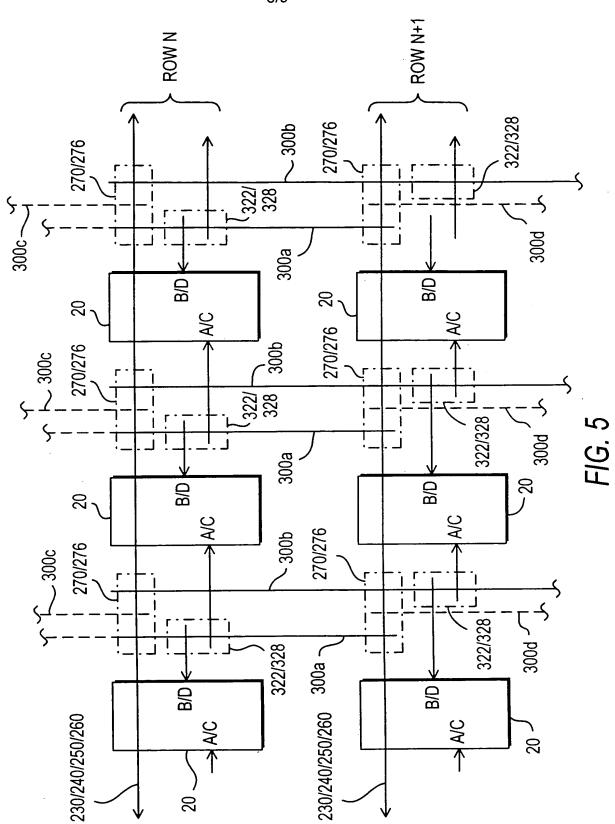


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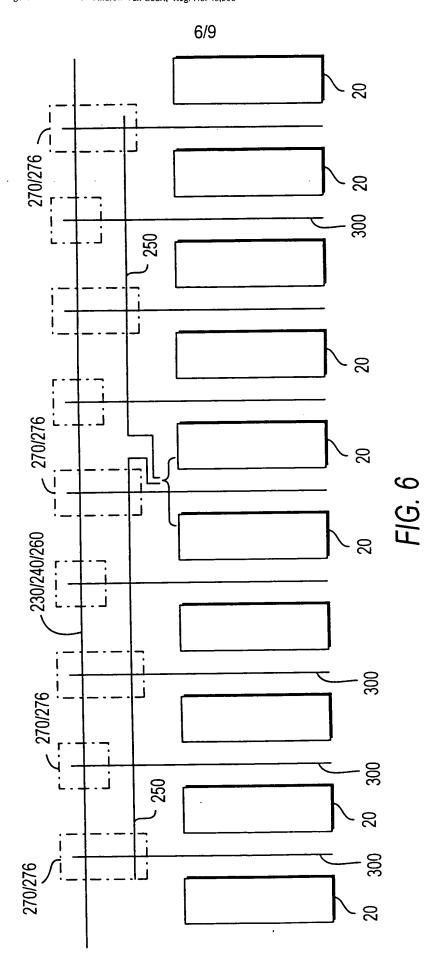
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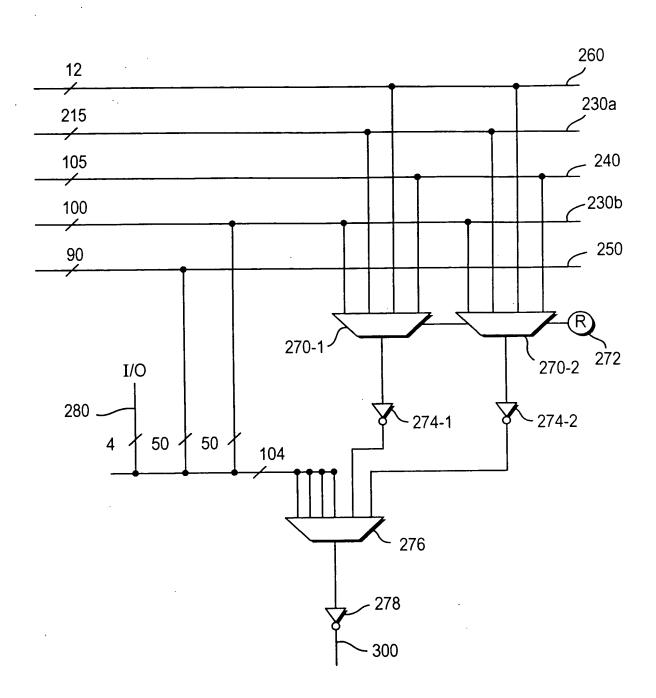
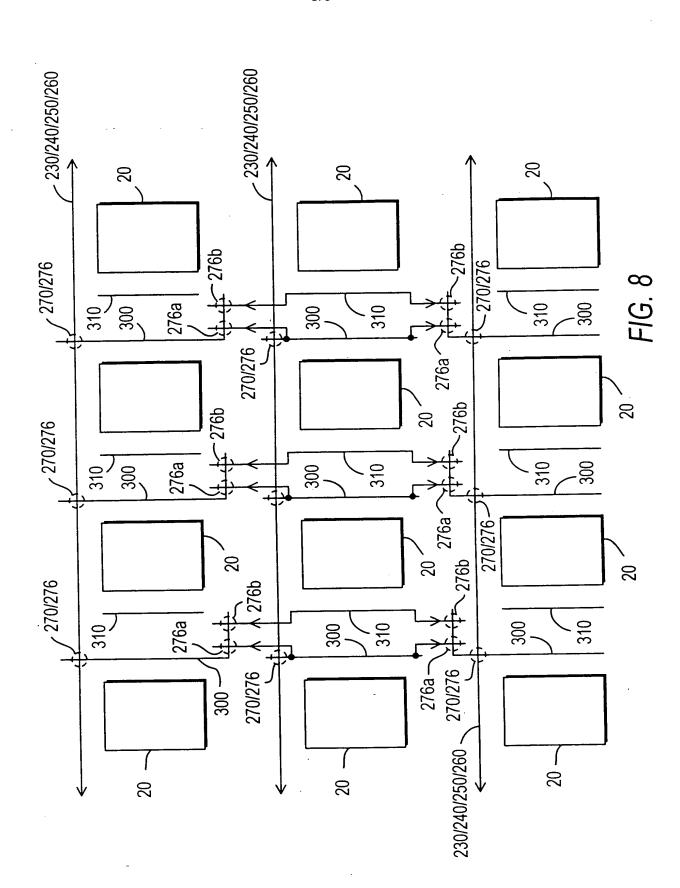


FIG. 7

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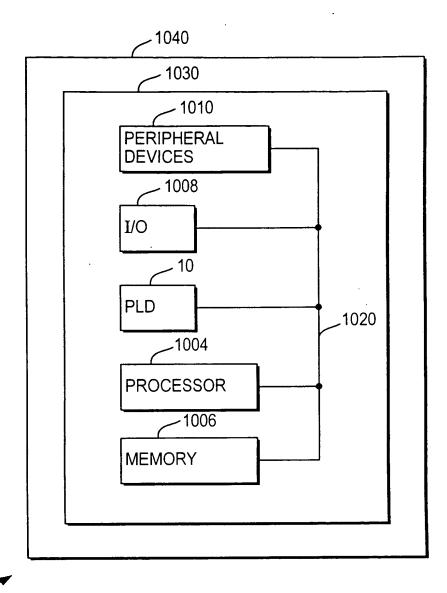
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FIG. 9